

**Notice of References Cited**

 Application/Control No.  
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 Applicant(s)/Patent Under  
 Reexamination  
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-				
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	Wolf, Stnaley, Silicon Processing for the VLSI Era 1990, Lattice Press, Vol. 2: Process Integration, p. 278	
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